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(54) Micro-miniature structure fabrication.

(57) In the fabrication of a free-standing miniaturized structure in a range of about 10 to 20 μm thick, a method based on a sacrificial system includes the steps of selecting a substrate material, depositing on the substrate material a sacrificial layer 58 of material and patterning the sacrificial layer to define a shape. A photoresist layer 62 of material is deposited on the sacrificial layer and patterned by contrast-enhanced photolithography to form a photores-

ist mould. Upon the mould there is plated a metallic layer 68 of material. The electroplated structure conforms to the resist profile and can have a thickness many times that of conventional polysilicon microstructures. The photoresist mould and the sacrificial layer are thereafter dissolved using etchants to form a free standing metallic structure in a range of about 10 to 20 μm thick, with vertical to lateral aspect ratios of 9:1 to 10:1 or more.

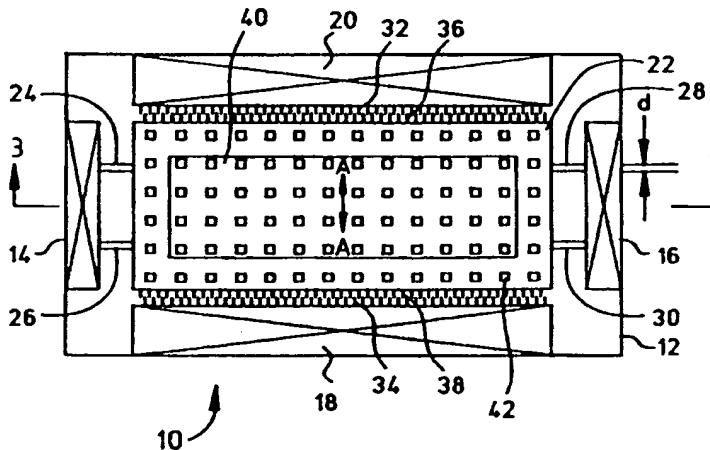


FIG. 1

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The present invention relates generally to micromachining and microfabrication technology, and more particularly to a method for fabricating a miniaturised structure.

Micro-technology encompasses a number of integrated microfabrication techniques derived from processes applicable to semiconductors and integrated circuits. These techniques include chemical etching, reactive ion etching, oxide etching, dry plasma etching, metallization, metal deposition, photolithography, thermal diffusion, ion implantation, and chemical vapour deposition. In recent years micromachining and microfabrication techniques have been used to advantage to provide a variety of active and passive microstructures in large quantities and at low cost. Among the many structures proposed are actuators and sensors, micro-motors and a variety of other devices including movable joints, levers, gears, sliders, springs and the like.

Historically, most of the surface-machined microstructures have been polysilicon-based. These structures are fabricated conventionally by chemical-vapour-deposited polycrystalline silicon films that are etched to produce structural layers on the order of 1-2 μm thick. These minimal thicknesses represent an inherent limitation of polysilicon-based structures which reduces their effectiveness in some applications. For example, polysilicon microactuators have been produced that are capable of generating constant forces of the order of sub-micro Newtons and torques of the order of sub-nano Newton-Meters. These devices can be used to move small objects at MHz frequencies. On the other hand, it would be desirable to move objects having a size of one cubic millimeter or more at acceleration rates as high as 100g. For that purpose, forces of the order of milli-Newton will be required. The fabrication of microstructures at this level requires a design providing sufficient vertical or out-of-plane rigidity, yet very low mechanical stiffness in the direction of acceleration such that the energy available for moving an object is maximized.

U.S. Patent No. 5,025,346 discloses the construction of laterally driven resonant microactuator structures using polysilicon thin-film techniques in a sacrificial system. Fabrication is based on a four mask process wherein a 2 μm thick layer of polysilicon is deposited by low pressure chemical vapour deposition (LPCVD) over a sacrificial layer of phosphosilicate glass (PSG). Following appropriate patterning, the sacrificial layer is dissolved to yield a free standing actuator structure and suspension system. The suspension system includes a plurality of polysilicon beams that support the actuator for high frequency lateral movement in the plane of the structure. At a vertical thickness only 2 μm .

however, very small vertical stiffness, that is, stiffness normal to the desired direction of acceleration, is provided by the suspension beams. This low vertical stiffness causes large vertical movements which are not desirable. Fortunately, as thickness in a given direction increases, the mechanical stiffness in that direction increases as a function of the cube of the thickness. On the other hand, stiffness in the orthogonal direction increases only linearly.

Thus, in microstructure applications, particularly those having free standing structures, it would be desirable to increase vertical structural thickness. This will require fabrication methods that overcome the thickness limitations of conventional thin film deposition techniques. Moreover, due to constraints on structural thickness in the direction of acceleration, techniques enabling the fabrication of structures having high aspect ratios (i.e., the ratio of vertical to lateral thickness) will be required. Although various x-ray techniques have been proposed for generating structures having thicknesses on the order of 100 μm , these thicknesses are much larger than necessary. Rather, it would be desirable to provide microstructures having vertical thicknesses on the order of 10 μm at an aspect ratio of about 10:1.

It is therefore an object of the invention to provide a method for the fabrication of free-standing miniaturized structures which provides structures having increased vertical thickness and stiffness.

Accordingly, the invention provides a method for fabricating a miniaturised structure, comprising the steps of: depositing a sacrificial layer of material on the surface of a substrate material; patterning the sacrificial layer to define a shape; depositing on the sacrificial layer a photoresist layer of material; patterning the photoresist layer by contrast-enhanced photolithography to form a photoresist mould; plating a metallic layer of material on the photoresist mould; and dissolving the photoresist mould and the sacrificial layer using etchants to form a free standing metallic structure.

Using the method of the present invention, the thickness constraints inherent in polysilicon thin-film fabrication techniques of the prior art are circumvented. Vertical thicknesses on the order of 10 μm (between about 10 and 20 μm) at an aspect ratio of about 10:1 are obtained in preferred structures. This advantage has been achieved through the use of alternative materials and an advanced lithographic and plating technique which permits thick-structure micromachining and an increase in structural thickness without sacrificing minimum in-plane features.

Embodiments of the present invention will now be described, by way of example only, with refer-

ence to the accompanying drawings in which:

Fig. 1 is a diagrammatic plan view of a microactuator structure fabricated in accordance with the present invention.

Figs. 2A-2M illustrate various stages of construction of the microactuator structure of Fig. 1. Fig. 3 is a cross-sectional view taken along line 3-3 in Fig. 1.

Fig. 4 is a diagrammatic end view of a slider tracking system for a magnetic and/or optical storage device.

Fig. 5 is a diagrammatic side view of a head tracking system for a magnetic and/or optical data storage system.

Fig. 6 is a diagrammatic plan view of an area-efficient multi-electrode microactuator system constructed based on the present invention.

Fig. 7 is a detailed view of a portion of the area-efficient microactuator of Fig. 6.

Referring to Figure 1, a miniaturized structure is formed as an electrostatic microactuator device 10. The device 10 includes a substrate 12 having mounted thereon a pair of spaced metallic anchors 14 and 16 disposed at opposing longitudinal ends of the substrate 12, and a pair of spaced metallic stators 18 and 20 formed on the substrate between the anchors 14 and 16 at opposing lateral ends of the substrate 12. There is suspended between the anchors 14 and 16 a free standing metallic actuator 22 which is mounted to the anchors 14 and 16 via a plurality of metallic suspension members 24, 26, 28 and 30. The suspension members are flexible beams joining the actuator 22 to the anchors 14 and 16 so as to allow in-plane movement in the direction of the arrow designated A-A in Fig. 1. In order to facilitate that motion, the thickness of the suspension members 24-30, represented by the dimension "d" in Fig. 1, is relatively thin. Thus, whereas the maximum dimension of the microactuator structure is about 1 millimeter, the thickness "d" is about 1 micrometer. This ensures sufficient suspension flexibility to enable the actuator 22 to be driven in the direction indicated. The driving force used to position the actuator is provided by the stators 18 and 20. Both stators include a plurality of inwardly extending electrodes 32 and 34, respectively. The actuator 22 also includes a plurality of outwardly extending electrodes 36 and 38. The actuator electrodes 36 and 38 are interleaved or "interdigitated" with the stator electrodes 32 and 34, respectively. By application of a DC voltage potential between the stators 18 and 20 and a conductor positioned below the actuator 22 (not shown in Fig. 1), an electrostatic force is generated that causes actuator movement in the direction A-A depending on the polarity of the applied potential, as is conventionally known.

To complete the actuator structure 10, a bonding platform 40 is formed on the top surface of the actuator 22 for bonding components to be driven by the actuator 22. It will be further noted that the actuator 22 includes a plurality of etch holes 42 whose purpose is to let etchant pass through to undercut the sacrificial layer underneath in a final release step, to be described in more detail below.

Turning now to Figs. 2A-2M, a preferred method for fabricating the actuator structure 10 is shown. In Fig. 2A is shown an insulated substrate 12 which can be made in conventional fashion from a silicon wafer. The substrate 12 is passivated with silicon dioxide and silicon nitride overcoats 50 and 52, respectively. The thickness of the substrate 12 may vary but is typically around 100 to 500 micrometers. The thickness of the silicon dioxide overcoat is about 500nm (5,000 angstroms) and the thickness of the silicon nitride overcoat is about 0.5 micrometers. On top of the silicon nitride overcoat 52, there is deposited a thin conducting layer 54 having a thickness of about 0.5 micrometers. The metallic conducting layer 54 could be a doped semiconductor material such as p-type polycrystalline silicon. Alternatively, this layer can be deposited by sputtering a selected metallic composition which may include any of the transition elements including copper or chromium.

Following deposition, the metallic layer 54 is patterned by photolithography and a plasma etching process. As shown in Fig. 2B, a photoresist layer 56 is formed on the metallic layer 54 and a first optical mask 57 is positioned over the photoresist layer. The photoresist material may be of any conventional type. These materials have in common the property that when exposed to an appropriate light source, they become less resistant to subsequent photodevelopment that would dissolve the photoresist material. The mask may be formed from conventional materials such as chrome on quartz. The photoresist layer 56 is irradiated using a collimated light source directed downwardly along an axis which is normal to the plane of the structure. A variety of collimated light sources may be used to expose the photoresist material including light in the ultra-violet region having a wavelength of around 365 nanometers. The exposed resist is developed and hard baked at about 120°F for approximately one (1) hour. A conventional resist solvent is used to dissolve the exposed resist layer to form a resist pattern as shown in Fig. 2C. Typically, photoresist manufacturers also provide suitable resist solvents that are compatible with the resist material being offered.

The patterned photoresist layer 56 is used for patterning the metallic layer 54 by plasma etching. Carbon tetrachloride (CCl₄) plasma etching is preferred. This process removes the portions of the

metallic layer 54 that are not protected by the photoresist layer 56. Following the plasma etching process, the photoresist layer 56 is dissolved, yielding a patterned metallic layer 54 as shown in Fig. 2D.

On top of the conducting layer 54 and the silicon nitride overcoat 52, there is deposited a sacrificial layer 58 made from a glass material and preferably phosphosilicate glass or PSG. This construction is illustrated in Fig. 2E. The PSG sacrificial layer is deposited by low pressure chemical vapour deposition (LPCVD) or plasma enhanced chemical vapour deposition (PECVD). Following deposition, the sacrificial layer 58 is patterned into a plate form that will be used to define the interior dimensions of the anchors 14 and 16 and the stators 18 and 20.

Referring to Figure. 2F, the sacrificial layer 58 is coated with a photoresist layer 60 and patterned by conventional photolithography using layers, followed by irradiating the photoresist and sacrificial layers using a collimated light source directed downwardly along an axis which is normal to the plane of the structure. Again, for this purpose, any conventional photoresist material and any conventional mask material, such as chrome on quartz, can be used. A variety of collimated light sources may also be used to expose the photoresist material including light in the ultraviolet region having a wavelength of around 365 nanometers. The exposed resist is developed and hard baked at about 120° F for approximately one (1) hour. A hydrogen-fluoride (HF)-based solution such as hydrofluoric acid is used to dissolve the exposed PSG layer. Finally, the remaining photoresist is dissolved in a resist solvent to yield a patterned sacrificial layer 58 as shown in Fig. 2G.

On top of the sacrificial layer 58 and the silicon nitride layer 52, an electroplating seed layer (not shown) is deposited by sputtering. The plating seed layer can be formed from one of the metallic elements and is preferably made from copper, nickel or chromium. Once the plating seed layer is deposited, a photoresist layer 62 is deposited over the seed layer, as shown in Fig. 2H. The photoresist layer 62 extends over the entire footprint of the substrate 12 with the sacrificial layer 58 being disposed centrally underneath. The thickness of the photoresist layer 62 will determine the thickness of the metallic components including the anchor members 14 and 16, the stators 18 and 20, and the actuator 22. Thus, for example, the photoresist layer 62 may be on the order of about 10 micrometers thick or in a range of 10 to 20 micrometers thick. Preferably, the photoresist layer 62 is not less than about 2 micrometers in thickness. The photoresist layer 62 may be formed from conventional photoresist materials including products sold

under the brand names AZ 4000 and AZ 6000.

The photoresist material is spun onto the structure in conventional fashion. Thereafter, as shown in Fig. 2H, a barrier layer 64 which can be made from soap material or the like is spun onto the photoresist layer. A contrast enhancement layer 66 is then spun onto the barrier layer 64. The contrast enhancement layer 66 is used to provide sharp optical contrast during subsequent photolithography in order to develop very sharp-edged, straight sided wall surfaces in the photoresist layer. This results in a form of contrast-enhanced lithographic structure. The contrast enhancement layer 66 is preferably made from one or more bleachable dyes having, for example, diaryl nitrones therein. The expression "bleachable dye" refers to the fact that the contrast enhancement layer becomes transparent when exposed to radiation from a photolithographic optical source above a critical energy level and remains opaque at lower radiant energies. The contrast enhancement layer functions as an optical switch which, upon activation, allows photodevelopment of the photoresist material below. This facilitates uniform photodevelopment of the underlying photoresist material so that upper portions are not overexposed while lower portions remain underexposed. A preferred contrast enhancement material is available from General Electric Corporation under the trademark "CEM 388". The critical energy level will vary with the thickness of the photoresist and the contrast enhancement layer.

Referring now to Fig. 2I, a third optical mask 68 is used to pattern the photoresist layer 62, the barrier layer 64 and the contrast enhancement layer 66 to form a mould for subsequent electroplating. The third optical mask 68 can be made conventionally from chrome on quartz, with the photoresist layer 62, the barrier layer 64 and the contrast enhancement layer 66 serving as a composite positive photoresist layer. By appropriate radiation exposure and selective resist hardening, exposed areas of the composite photoresist layer are dissolved by subsequent photo development and hardening while portions of the resist underneath the third optical mask 68 remain intact. The radiation source of choice for patterning the composite photoresist layer is a low numerical aperture (NA) projection system. The system provides highly collimated radiation in the ultraviolet region and preferably at a wavelength of about 365 nanometers directed substantially normal to the plane of the structure. The exposed resist is thereafter developed but is not hard baked.

The patterning of the composite photoresist layer yields a mould structure, as exemplified by Fig. 2J, that defines the shape and configuration of the metallic anchors 14 and 16, the actuator 22 and the stators 18 and 20, shown in Fig. 1. Moreover,

the plural etch holes 42 are defined. Using the described photolithographic patterning process, photoresist stencils or molds up to 20 μm thick, with aspect ratios of 9:1 to 10:1 and even higher can be readily achieved, thus permitting the development of metallic structures that are fairly thick in the out-of-plane direction (relative to Fig. 1) while allowing very thin features, such as the spacing of the interleaved electrodes, to be formed in the plane of the structure. With the photoresist formed in the manner described, a metallic structure 69 can be deposited as shown in Fig. 2K by electroplating using the plating seed layer as an electroplating electrode. The plating material can be any of the metallic elements and is preferably a transition element such as copper, nickel or chromium when the sacrificial layer is phosphosilicate glass.

Referring to Fig. 2L, following the metallic plating step, the bonding platform 40 is created by depositing a photoresist layer 70 on the plated metallic layer 69. The photoresist layer 70 is made from a conventional resist material. A fourth optical mask 72 is placed over the photoresist layer 70. Again, the optical mask is a conventional material such as chrome or quartz. The photoresist layer 70 is exposed to a collimated light source providing light in the ultra-violet region having a wavelength around 365 nanometers. This light is directed downwardly along an axis which is normal to the plane of the structure. The exposed resist is thereafter developed but is not hard baked. The patterned resist is then used as an electroplating mould to form a metal structure 74, as shown in Fig. 2M. This structure forms the bonding platform 40 when the resist layer 70 is dissolved.

Following construction of the bonding platform 40, the remaining composite photoresist layer, including the layers 62, 64 and 66, the seed layer and the sacrificial layer 58, are all dissolved to form the metallic anchor structures 14 and 16, the stator structures 18 and 20, and the free standing actuator structure 22, including the suspension members 24-30 and the interleaved electrodes 32-38. A cross-section of this configuration is shown in Fig. 3. The etchant used to dissolve the sacrificial layer 58 is preferably hydrofluoric acid or any other suitable HF-based solution when the sacrificial layer is made from phosphosilicate glass and the metallic structure 69 is made from copper or chromium. These materials advantageously provide an etch rate of around 1 micrometer per minute or greater in the sacrificial material at room temperature, and a negligible etch rate of the copper or chromium metallic structure. Thus, the hydrogen fluoride-based etchant provides good etching selectively with respect to a PSG sacrificial layer 58 and a Cu or Cr metallic structure 69.

It will be appreciated that structures having high aspect ratios can be conveniently formed using the fabrication method described above. High aspect ratio structures such as the microactuator 10 are expected to have numerous applications wherein it is desirable to minimize vertical or out-of-plane deflection while generating good in-plane actuating forces. For example, high aspect ratio microactuators could be economically used in sub-micrometer tracking for magnetic or optical recording. Examples of this application are illustrated in Figs. 4 and 5.

Fig. 4 illustrates a slider tracking system 100 showing a microactuator 102 constructed in accordance with the invention, sandwiched between a tracking arm suspension assembly 104 and a slider 106 having a pair of magnetic heads 108 and 110 mounted thereon. The microactuator 102 is shown as including a substrate 112, a free standing actuator structure 114 and a bonding platform 116 to which the slider 106 is mounted. The device of Fig. 4 can be constructed to have large vertical stiffness (40 kN/m) and 1 mN force yielding a 1.6 kHz bandwidth for 1 micrometer tracking with a 10 milligram load.

An alternative construction for a head tracking system 120 is illustrated in Fig. 5. A slider 122 is shown as having a lower air bearing surface 124. A thin film head 126 is mounted to the slider 122 via a microactuator structure 128 constructed in accordance with the present invention. Again, extremely fine tracking corrections are provided by the microactuator 128.

Figs. 6 and 7 illustrate a preferred microactuator construction for developing in-plane actuating forces, in this case, an area-efficient, multi-electrode microactuator structure 130. This electrode tree structure includes a pair of stators 132 and 134 each having multiple branches 136 and 138, respectively. Each branch in turn has plural fingers 140 and 142 extending laterally outwardly therefrom as more clearly shown in Fig. 7. The tree structure further includes an actuator 144 having plural branches 146 each having plural electrode fingers 148 extending laterally therefrom, as also illustrated in Fig. 7. The actuator 144 is mounted by plural suspension members 150, 152, 154 and 156 to a pair of anchor structures 158 and 160. In this approach, each pair of fingers forms an energy conversion cell which popularizes the whole area and efficiently converts electrical field energy into force or torque and maintains a constant output versus input relation within a small operational range. It is estimated that if the structure of Figs. 6 and 7 is sized to encompass an area of 1 millimeter by 1 millimeter with an out-of-plane thickness of 20 micrometers, electrodes having a width of 6 micrometers, with 2 micrometer air gaps, a 10

micrometer branch width, a branch separation of 25 micrometers for the interdigitated fingers, and 5 micrometers otherwise, and a maximum voltage of 100 volts, a lateral force of 1.1 milli-Newtons could be achieved. For structural integrity, the electrode tree structure 130 could be provided with a top plate to hold all of the movable actuator electrode branches together. The plate could be either metal or formed from a process-compatible dielectric material. Assuming a 5 micrometer separation between the plate and stator electrode branches, it is estimated that a small vertical force of 0.3 milli-Newtons would result. The vertical stiffness, being at least three orders of magnitude larger than a comparable structure made from polysilicon, should be able to accommodate this load with minimal out of plane deflection.

Claims

1. A method for fabricating a miniaturized structure, comprising the steps of:
 depositing a sacrificial layer (58) of material on the surface of a substrate material (12);
 patterning the sacrificial layer to define a shape;
 depositing on the sacrificial layer a photoresist layer (62) of material;
 patterning the photoresist layer by contrast-enhanced photolithography to form a photoresist mould;
 plating a metallic layer (68) of material on the photoresist mould; and
 dissolving the photoresist mould and the sacrificial layer using etchants to form a free standing metallic structure.
2. A method as claimed in Claim 1 wherein the materials of sacrificial layer, the metallic layer and the etchants are chosen to provide high etching selectivity so as to etch the sacrificial layer at a substantially higher rate than the metallic layer.
3. A method as claimed in Claim 1 or Claim 2 wherein the sacrificial layer is formed from a glass material, the metallic layer is formed from a transition element and the etchant is made from an HF-based acid material.
4. A method as claimed in any preceding claim wherein the sacrificial layer is phosphosilicate glass (PSG), the metallic layer is copper or chromium and the etchant is hydrofluoric acid (HF).
5. A method as claimed in any preceding Claim wherein the step of depositing a photoresist layer further includes the step of depositing a contrast enhancement layer (66) on the photoresist layer.
- 10 6. A method as claimed in any of claims 1 to 4 wherein the step of depositing a photoresist layer further includes the step of depositing a barrier layer (64) on the photoresist layer and a contrast enhancement layer on the barrier layer.
- 15 7. A method as claimed in Claim 5 or Claim 6 wherein the contrast enhancement layer is formed from a composition having a bleachable dye therein.
- 20 8. A method as claimed in any preceding Claim wherein the step of patterning the photoresist layer includes masking the photoresist layer and exposing unmasked portions of the photoresist layer by means of a source of collimated radiation having a wavelength of about 365 μm.
- 25 9. A method as claimed in any preceding Claim wherein the photoresist layer is formed at a thickness of not less than about 2 μm.
- 30 10. A method as claimed in any preceding claim wherein the photoresist layer is deposited to a thickness in the range of 10 to 20 microns.
- 35 11. The method of Claim 1 wherein the photoresist layer is patterned so as to provide a mould for producing a free standing metallic structure having a maximum aspect ratio of at least 9:1.
- 40 12. A method for fabricating a miniaturized electrostatic actuator structure including a positionable actuator mounted by plural springs to a base, a stator structure and plural electrostatic drive elements between the actuator and the stator structure, the method comprising the steps of:
 securing an electrically conducting layer to an insulated substrate material;
 patterning the electrically conducting layer by photolithography using a first optical mask to form a ground electrode;
- 45 50 55
- 50 55
- 55

tuator to be formed;

plating a seed layer of a copper or chromium material on the sacrificial layer, the electrically conducting layer and the insulated substrate;

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depositing a first photoresist layer on the sacrificial layer;

depositing a barrier layer on the first photoresist layer;

depositing a contrast enhancement layer on the barrier layer;

patterning the first photoresist layer, the barrier layer and the contrast enhancement layer by photolithography using a third optical mask to define an actuator structure including an actuator, a base structure, plural springs connected between the actuator and the base structure to support the actuator from movement, the plural springs having an aspect ratio of at least 9:1, and to further define a stator and plural interdigitated electrostatic drive elements extending between the actuator and the stator;

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plating a layer of copper or chromium on the patterned photoresist layer;

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depositing a second photoresist layer on the layer of copper or chromium;

patterning the second photoresist layer by photolithography using fourth optical mask to form a bonding platform for securing components to the actuator structure; and

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dissolving the sacrificial layer using a hydrogen fluoride etchant to form a free standing metallic actuator mounted by the plural springs to the base structure.

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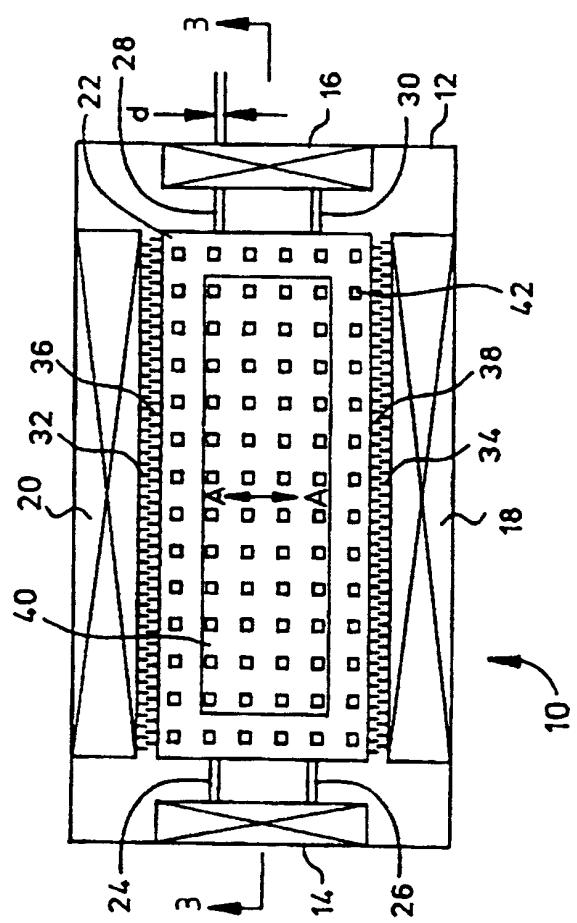
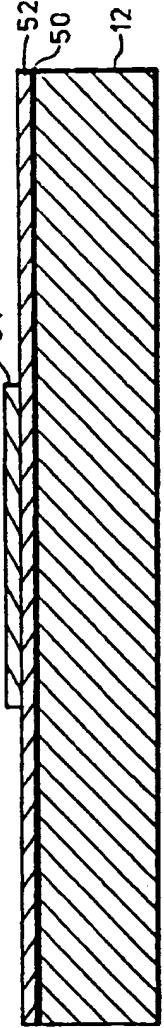
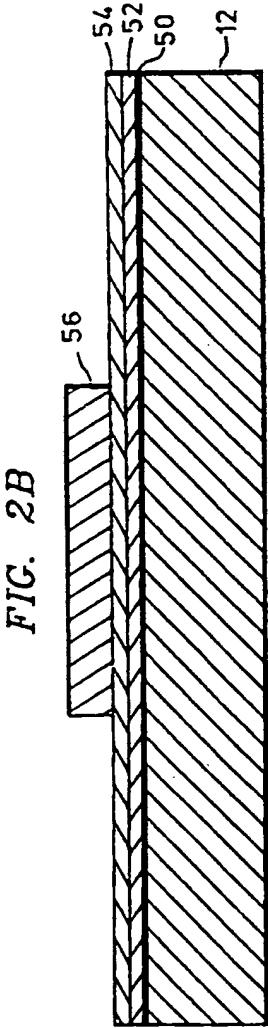
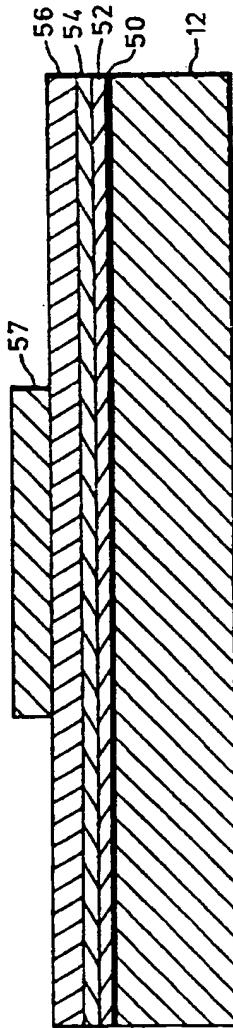
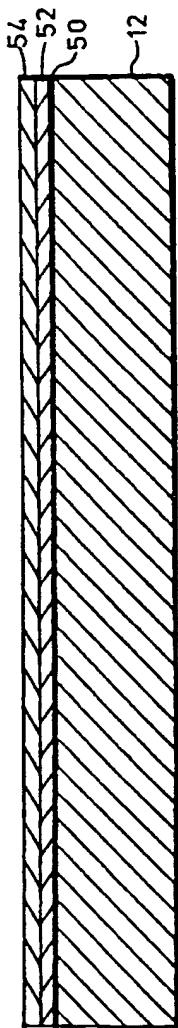
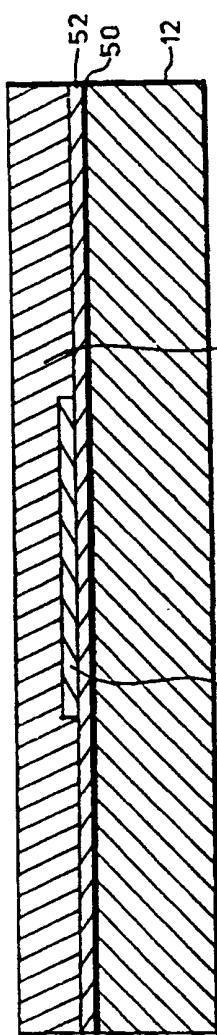
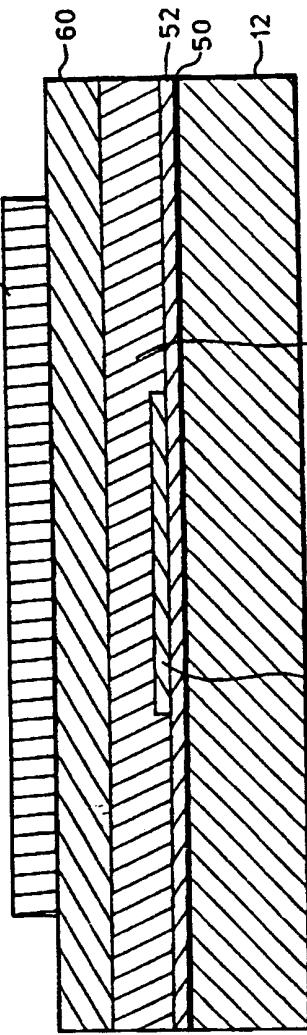


FIG. 1

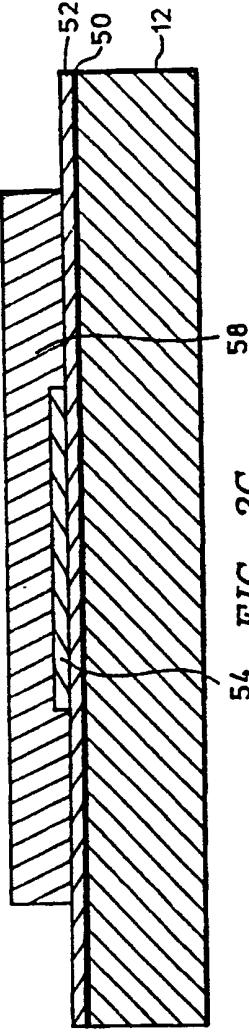




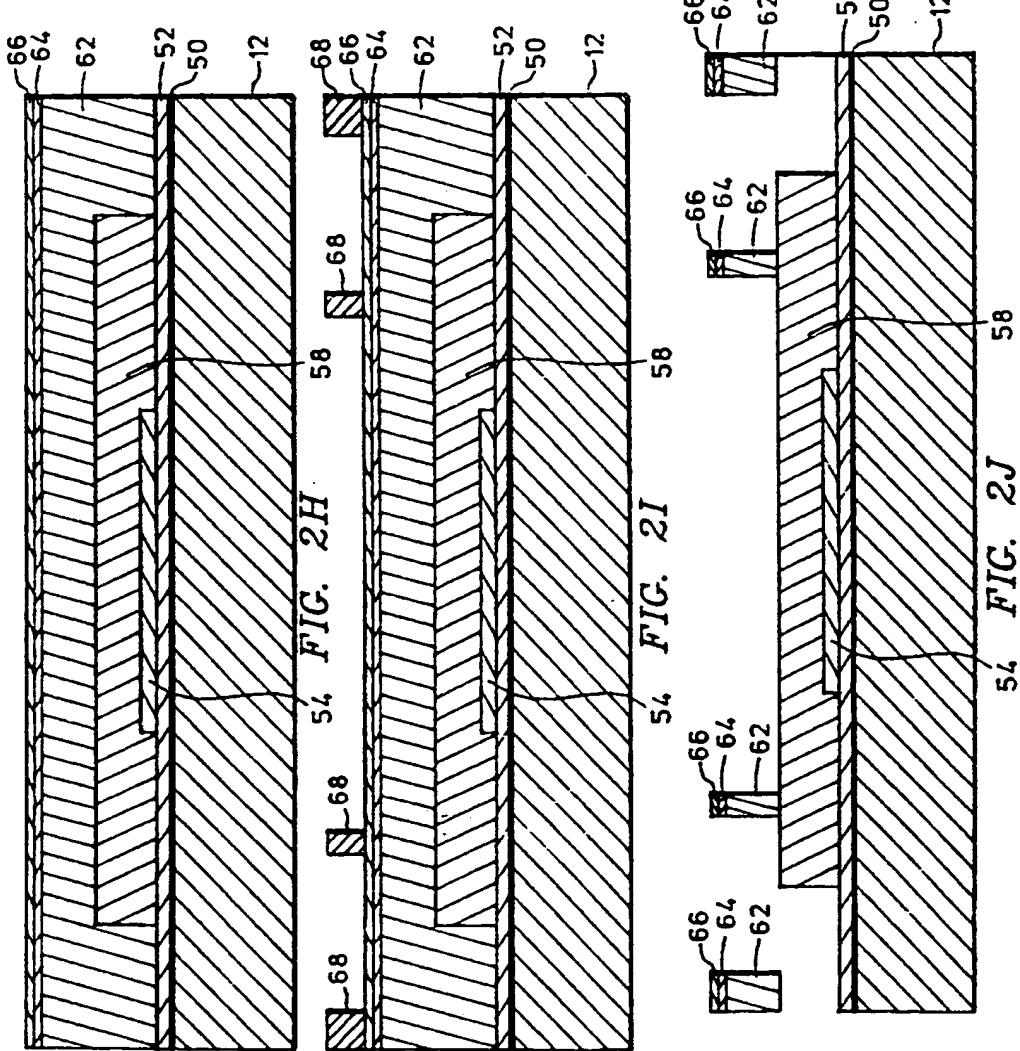
54 FIG. 2E



54 FIG. 2F



54 FIG. 2G



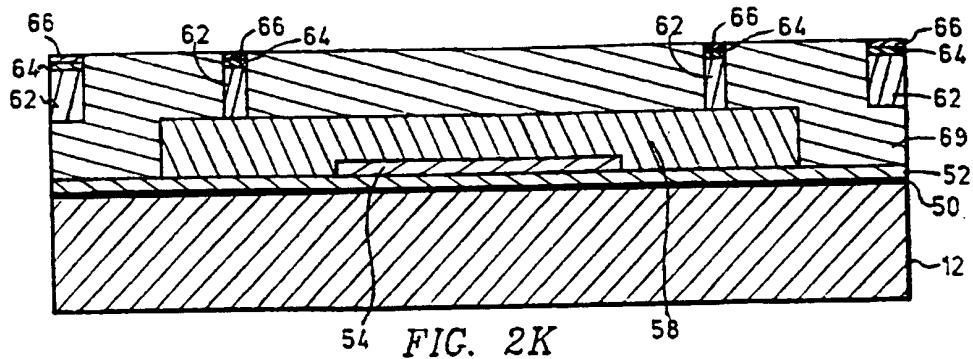


FIG. 2K

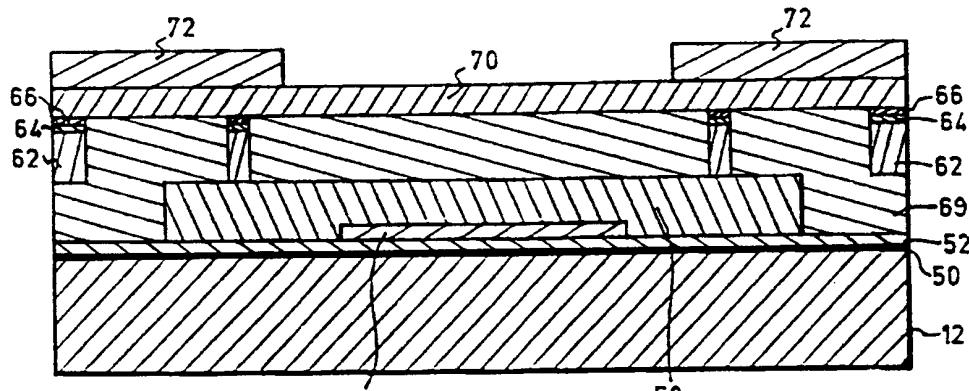


FIG. 2L

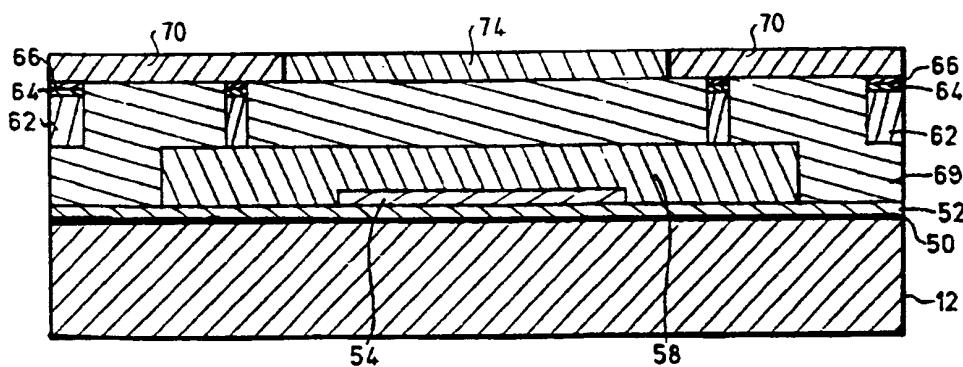


FIG. 2M

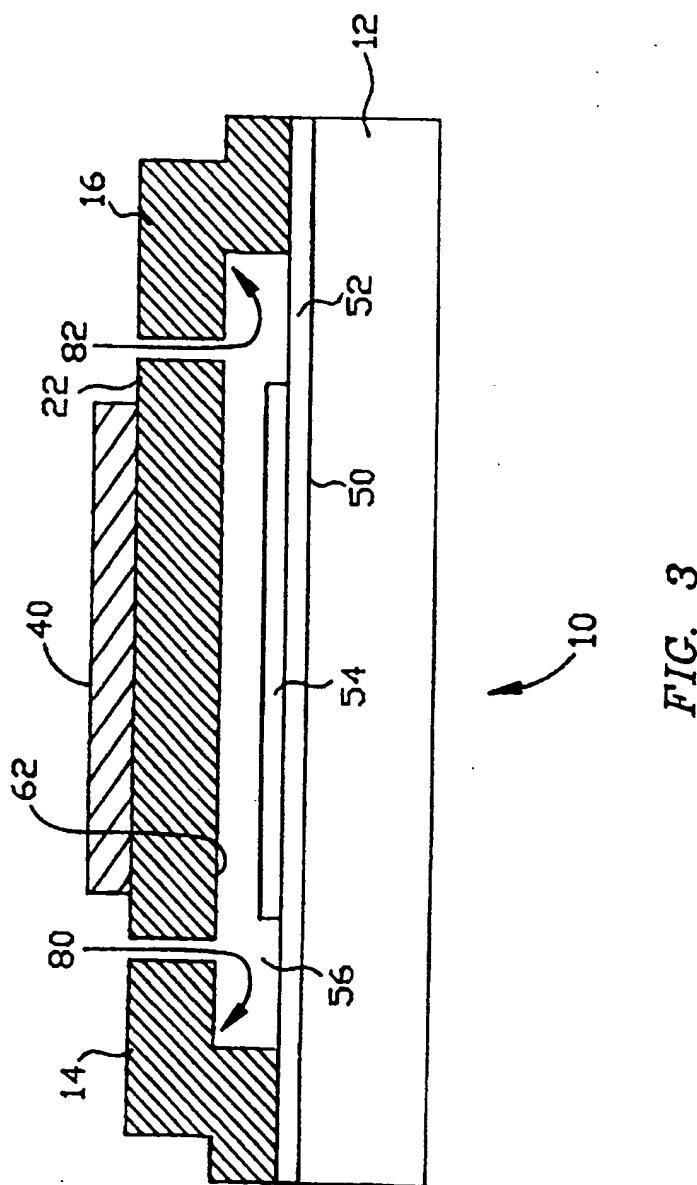
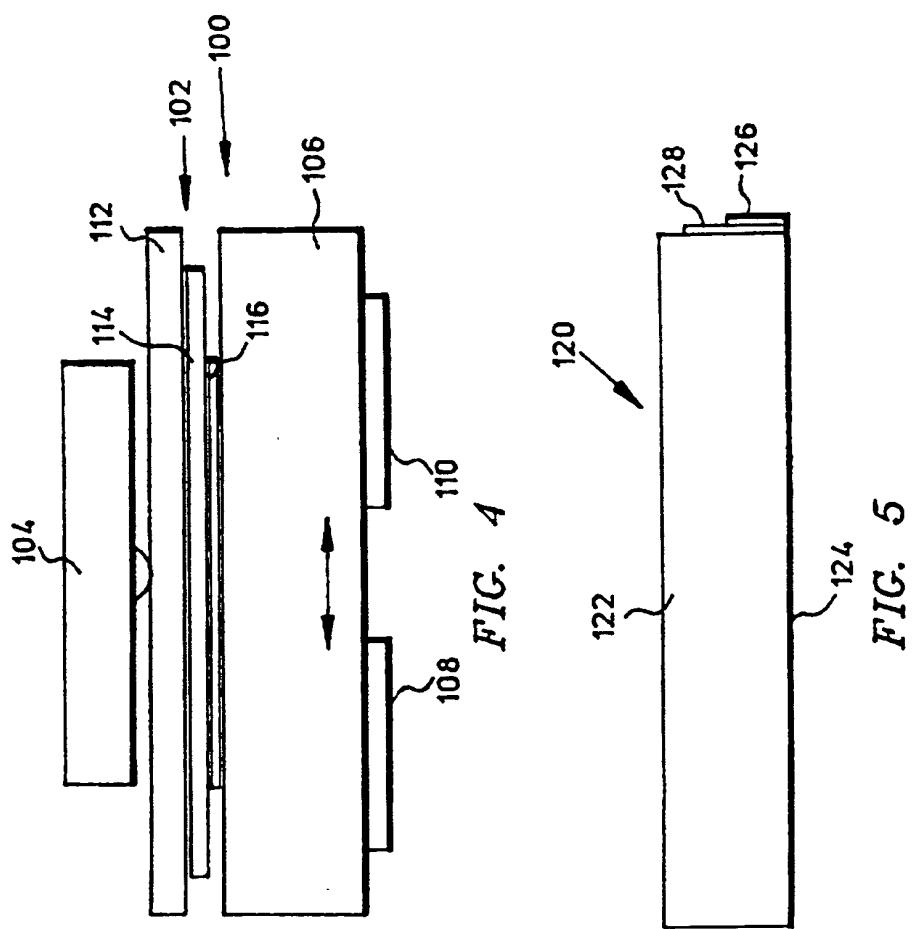


FIG. 3



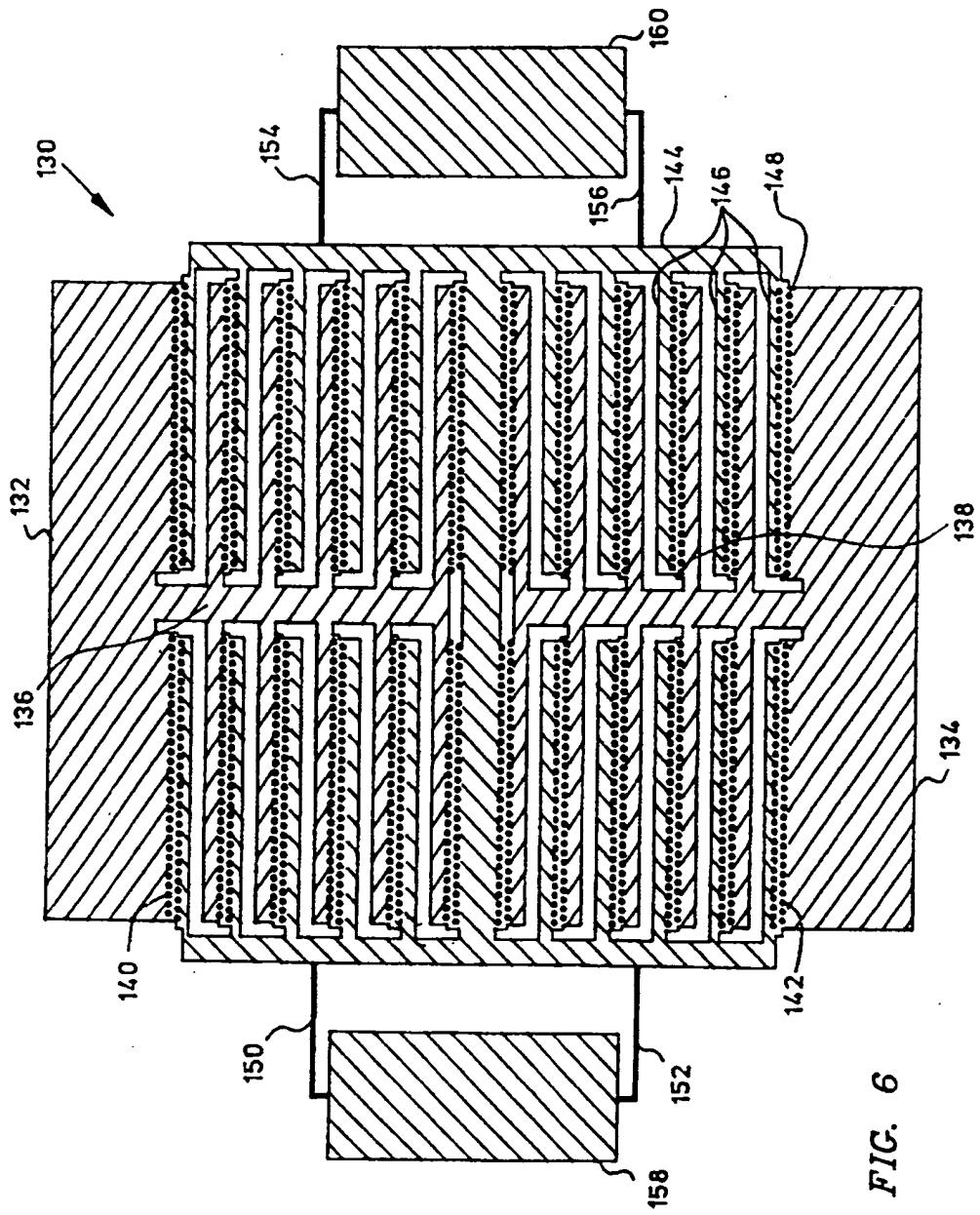


FIG. 6

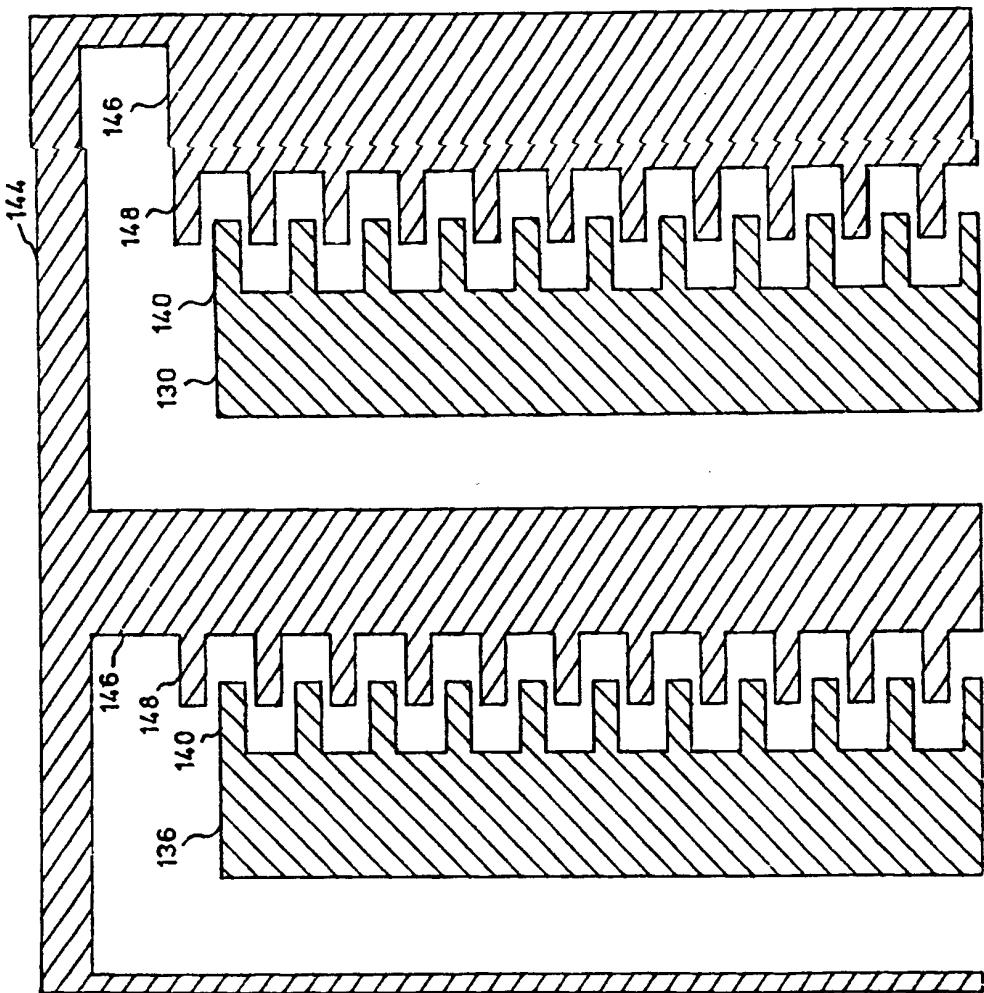


FIG. 7